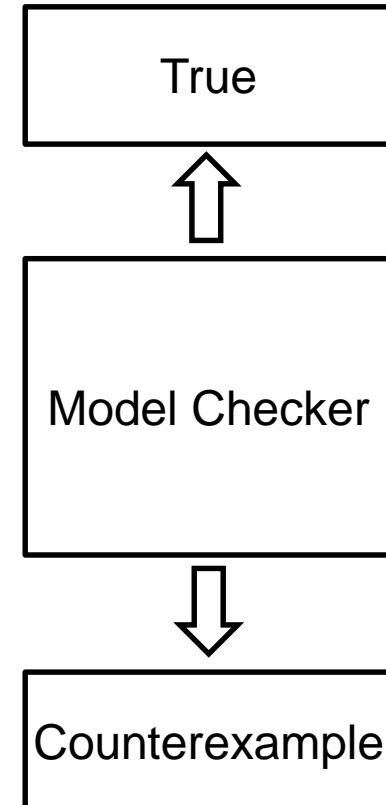
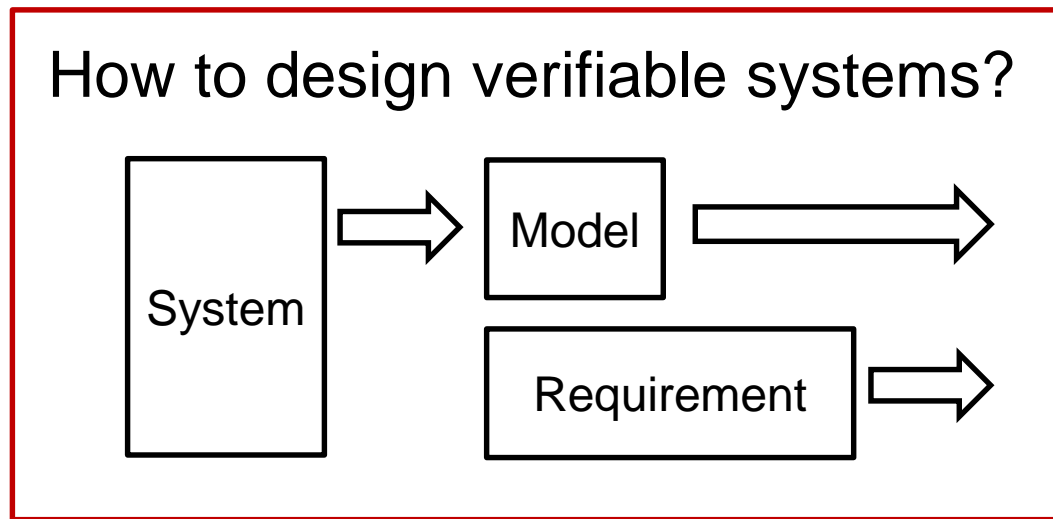


# On Efficiently Specifying Models for Model Checking

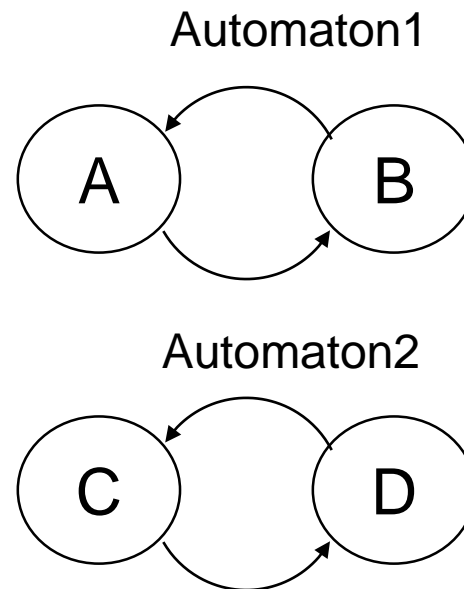
Mykhaylo Nykolaychuk, Michael Lipaczewski, Tino Liebusch,  
Frank Ortmeier

- **HOW TO ANALYZE A GIVEN MODEL WELL?**

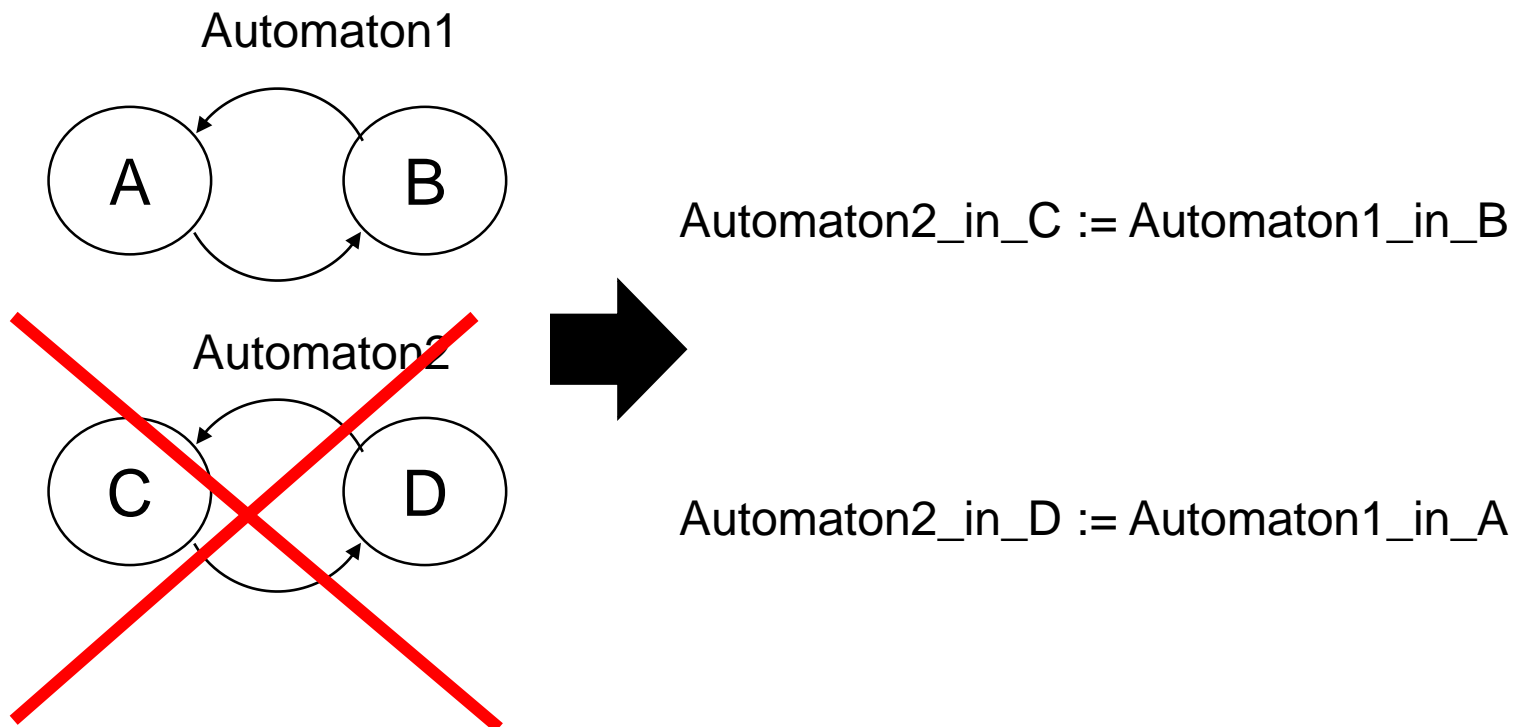


- **State-space explosion**
  - Exponential increase in memory usage and checking time with increasing model size
  - Composition of  $n$  processes with  $m$  states each results in  $m^n$  states
- **Straightforward modeling approach**
  - All system parts and processes as automata

- Example:  
Two components periodically switch their states.
- **Naive modelling: 2 automata**

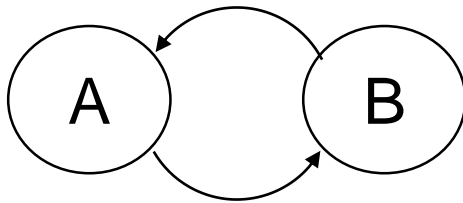


- Replace **specific automata** by **boolean formula** during the modeling process

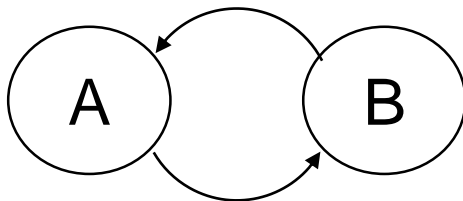
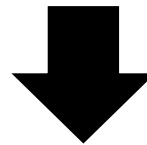
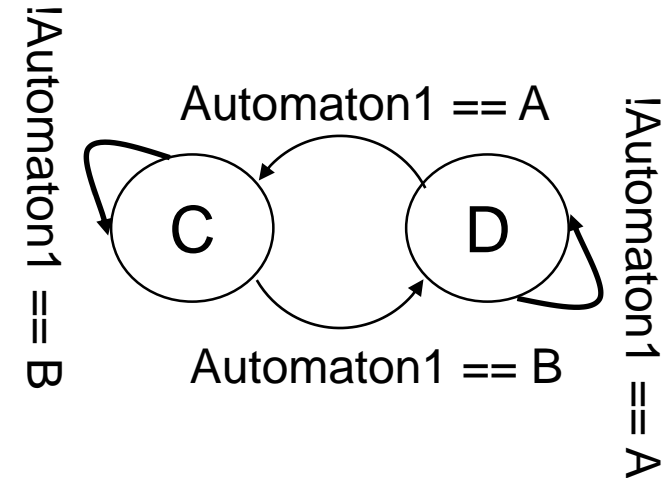


- One component triggers another (simple) component

Automaton1



Automaton2



Automaton2\_in\_C := Automaton1\_in\_B

Automaton2\_in\_D := Automaton1\_in\_A

- **Semantical difference**

**Before replacement**

	<b>A1_A</b>	<b>A1_B</b>	<b>A2_C</b>	<b>A2_D</b>
t1	0	1	0	0
t2	0	1	1	0

**After replacement**

	<b>A1_A</b>	<b>A1_B</b>	<b>A2_C</b>	<b>A2_D</b>
t1	0	1	1	0
t2	0	1	1	0

e.g., A1\_A is changed its state to A1\_B

**PROBLEM???**

- Formal models are derived from informal specifications
- Question:  
Is the time delay intended/required?

OR

Is the time delay only optional (and might have been caused by the modelling framework)?

→ **Guideline:** If in doubt, use formulas!



- **Algorithm:**

1. Use straightforward approach for initial system modeling
2. Localize replaceable automata that only forward information
3. Check if delay was intended and if not replace them by boolean formula and customize such substitution throughout the model

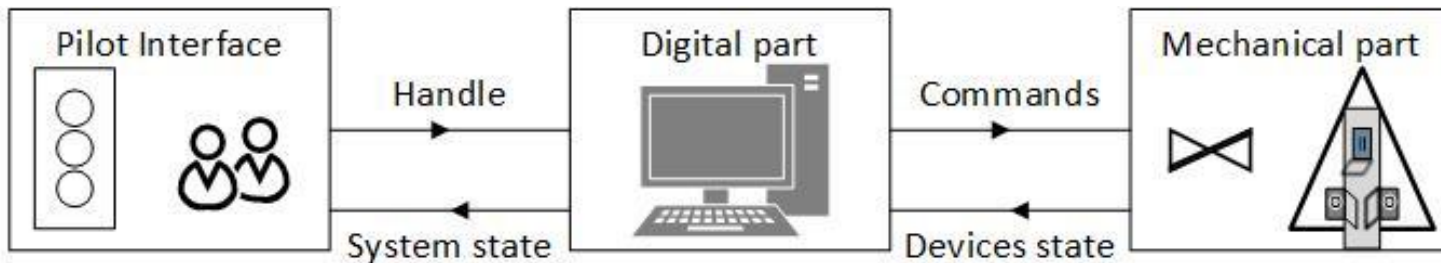
Necessary for consistent and efficient modelling:

- Clear namespaces
- Formula nesting

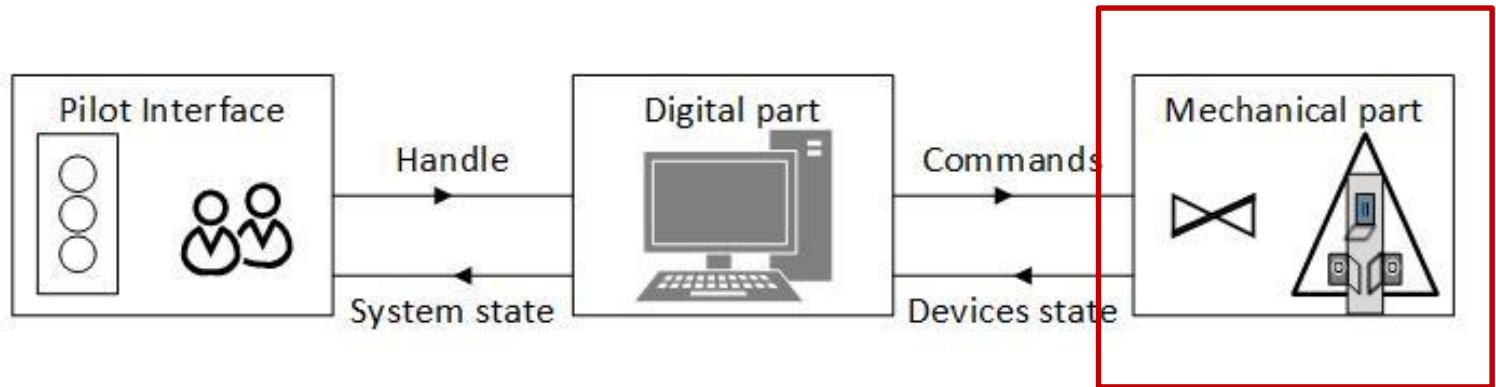
4. Check for cyclic dependencies

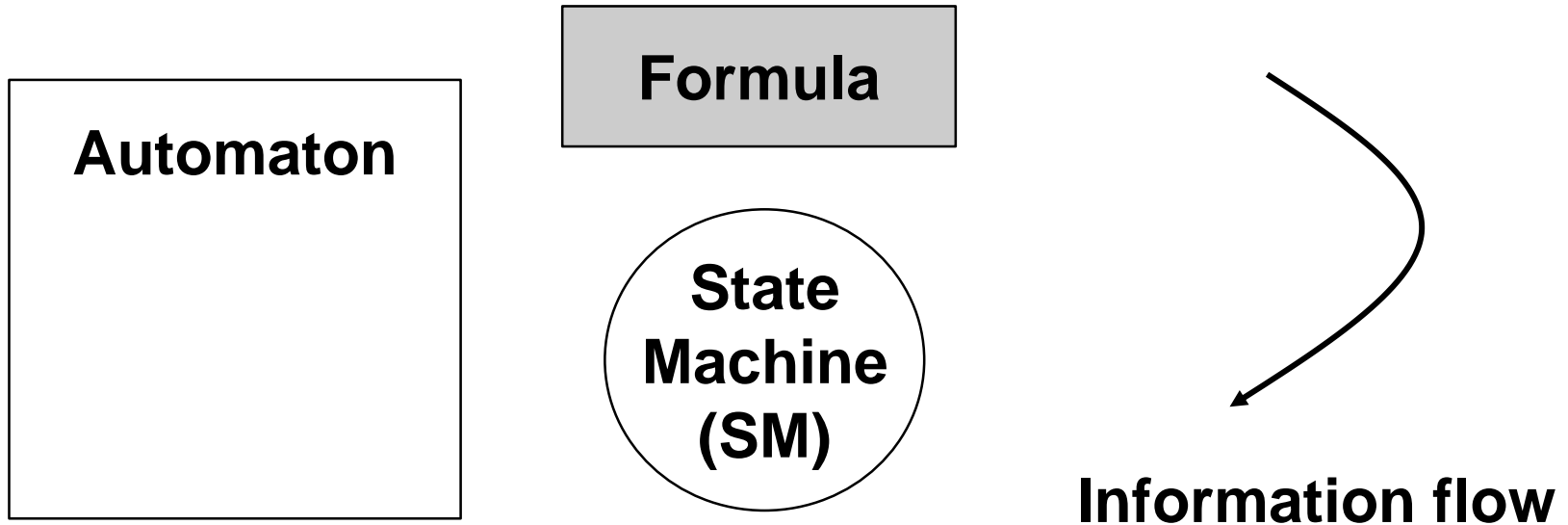
- System Analysis Modeling Language (SAML)
  - Unified qualitative and quantitative aspects of a system in a single model
- Verification Environment for Critical Systems (VECS)
  - Eclipse-based tool for design and verification of system models in SAML
  - Transformation of SAML model into input language for verification by existing tools (e.g., NuSMV, PRISM)

- Landing gear system – for maneuvering the gears and associated doors
- Three main components: pilot interface, digital part and mechanical part



## 1. Standard model – all components as automata





```
enum cylinder := [down , high , intermediate];
```

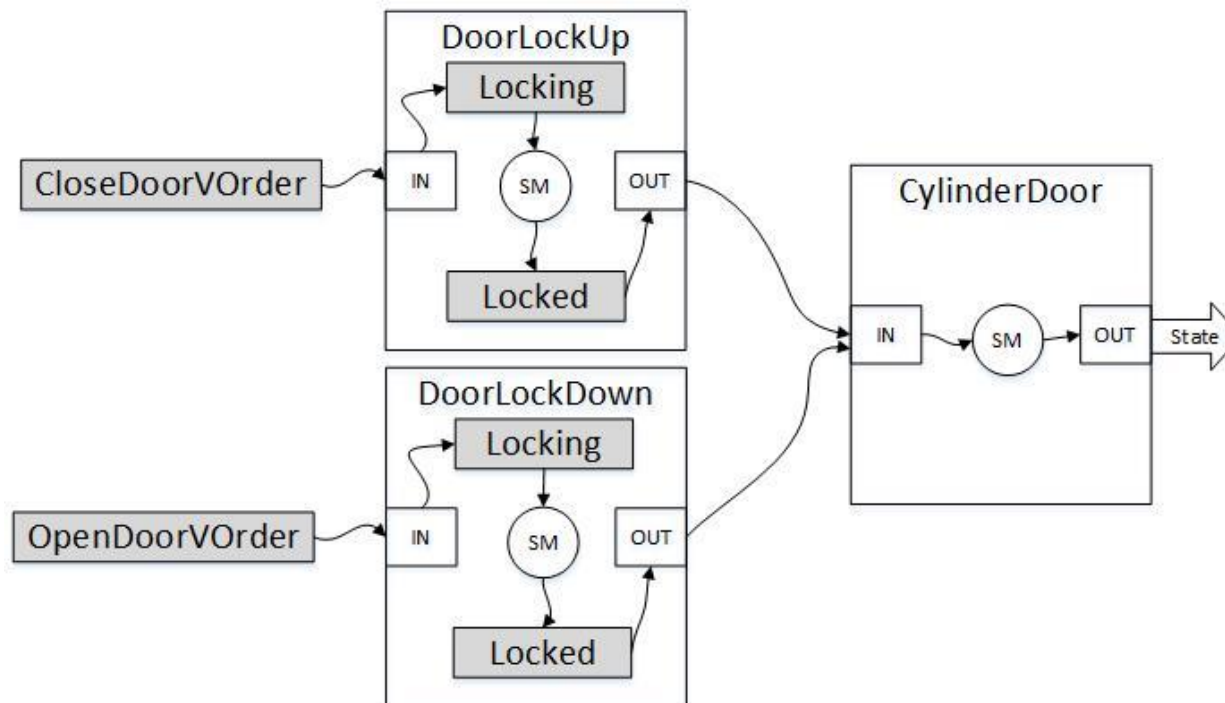
```
component CylinderDoor // Automaton
```

```
    cylinderState : cylinder init cylinder.down; // State machine
    // ... update rules for state machine
```

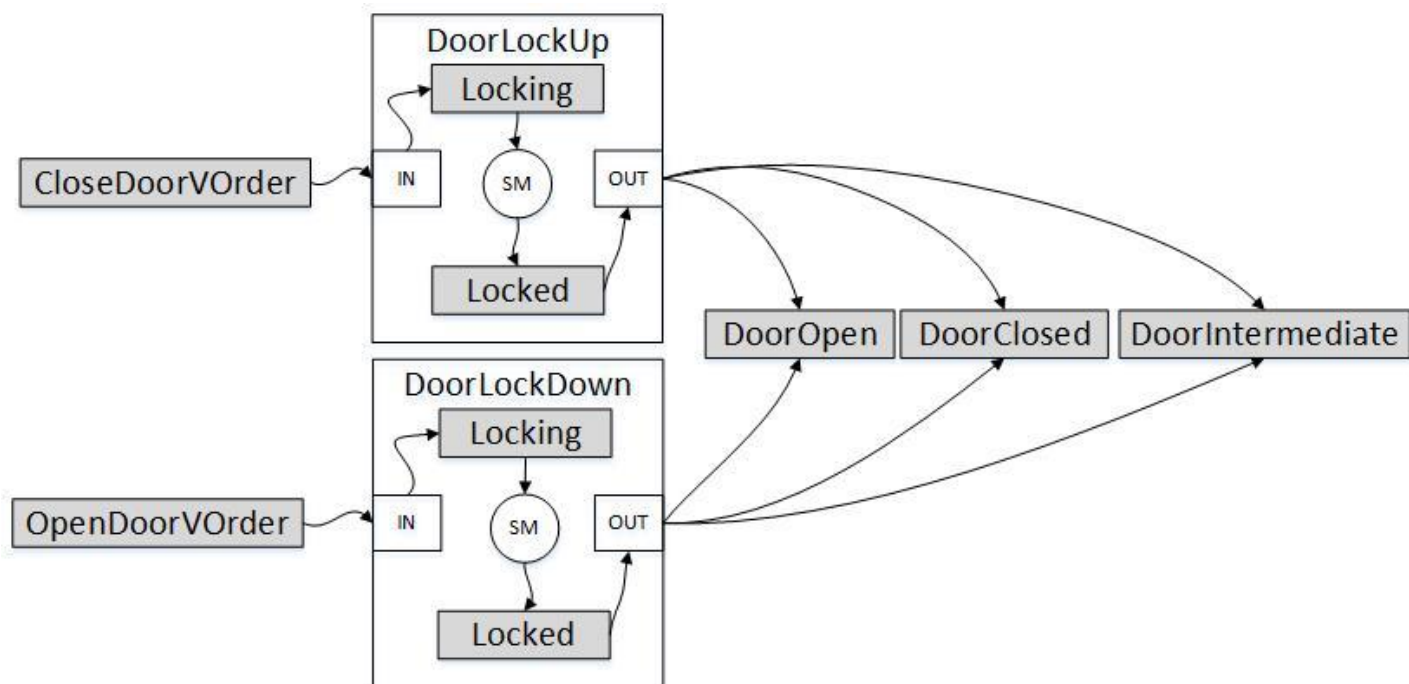
```
endcomponent
```

## 2. Localize replaceable automata

- CylinderDoor automaton
  - States: down, high, intermediate



## 3. Replace chosen automaton by boolean formula



## 4. Check for cyclic dependencies

- Formula may refer to itself
- Formula could contain other formulas which may refer to this formula



- Requirement as CTL
- Comparison of Standard model and Restructured model by
  - Reachable states
  - BDD size
  - Checking time

	Reachable States	BDD nodes	Time, s
SM	$2^{79}$	3830081	20
RM	$2^{68}$	806275	0.4

- Results:
  - Reachable states are reduced by factor **2<sup>11</sup>**
  - BDD size is smaller by factor **4.5**
  - Checking time is smaller by factor **45**

## Conclusion

- New directive for system design
- Semantic change
  - time delay after replacement
- Model size reduction
  - BDD and checking time become smaller

## Further work

- Scalability of our approach
- Ability to find candidates for replacement automatically

Questions?